



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Danial S. Dean

Serial No.: 10/633,956

Filed: August 4, 2003

For: DEVICE FOR MARGIN TESTING A
SEMICONDUCTOR MEMORY BY
APPLYING A STRESSING VOLTAGE
SIMULTANEOUSLY TO
COMPLEMENTARY AND TRUE DIGIT
LINES

Confirmation No.: 5171

Examiner: S. Mai

Group Art Unit: 2818

Attorney Docket No.: 2269-3318.6US
(97-0527.06/US)

Notice of Allowance Mailed:

April 30, 2004

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

July 22, 2004
Date

Signature

Deidra J. Pfeil
Name (Type/Print)

AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Serial No. 10/633,956

Amendments to the Specification begin on page 3 of this paper.

A Listing of the Claims begins on page 6 of this paper.

Remarks begin on page 7 of this paper.